





An ISO/TS 16949, ISO 9001 and ISO 14001 Certified Company

## **SOT-23 Formed SMD Package**

### **CMBT4126**

# GENERAL PURPOSE TRANSISTOR

P-N-P transistor

Marking CMBT4126 = 5E PACKAGE OUTLINE DETAILS ALL DIMENSIONS IN mm

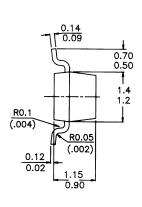


1 = BASE 2 = EMITTER

3 = COLLECTOR



2.8 0.48 0.38 3 2.6 2.4 1.02 0.89 0.60 2.00 0.40 1.80



#### ABSOLUTE MAXIMUM RATINGS

Collector-base voltage (open emitter)	$-V_{CBO}$	max.	25	V
Collector-emitter voltage (open base)	$-V_{CEO}$	max.	25	V
Emitter-base voltage (open collector)	$-V_{EBO}$	max.	4	V
Collector current (d.c.)	$-I_C$	max.	<i>200</i>	mA
Total power dissipation at $T_{amb} = 25^{\circ}C$	$P_{tot}$	max	<i>350</i>	mW
D.C. current gain				
$-I_C = 2 \text{ mA}$ : $-V_{CF} = 1 \text{ V}$	here	min.	120	
-1C - 2  mA, -vCE - 1  v	hFE	max.	360	

## **RATINGS** (at $T_A = 25^{\circ}C$ unless otherwise specified)

Limiting values

Collector-base voltage (open emitter)	$-V_{CBO}$	max.	25	V
Collector-emitter voltage (open base)	$-V_{CEO}$	max.	25	V
Emitter-base voltage (open collector)	$-V_{EBO}$	max.	4	V
Collector current (d.c.)	$-I_C$	max.	200	mA

Total power dissipation at $T_{amb} = 25^{\circ}C$	$P_{tot}$	max	350	mW
Storage temperature	$T_{stg}$	-55 to	+150	° C
Junction temperature	Tj	max.	<i>150</i>	° C
THE PLANT OF A CHARLES				
THERMAL CHARACTERISTICS				
$T_j = P(R_{th j-t} + R_{th s-a}) + T_{amb}$				
Thermal resistance	D .		550	9C/maN/
from junction to ambient	$R_{th\ j-a}$		556	°C/mW
<b>CHARACTERISTICS</b> (at $T_A = 25$ °C unless otherwise	se specified)			
Collector-emitter breakdown voltage				
$-I_C = 1 \text{ mA}; I_B = 0$	-V <sub>(BR)</sub> CEC	min.	25	V
Collector-base breakdown voltage	(==)===			
$-I_C = 10 \ \mu A; I_E = 0$	$-V_{(BR)CBC}$	min.	25	V
Emitter-base breakdown voltage	(210) 02 0			
$-I_E = 10 \ \mu A; I_C = 0$	$-V_{(BR)EBO}$	min.	4	V
Collector cut-off current	(BIO)EBC			
$-V_{CB} = 20 \ V; I_{F} = 0 \ V$	$-I_{CBO}$	max.	50	nΑ
Emitter cut-off current	CDC			
$V_{BE} = 3 \ V; I_C = 0$	$I_{EBO}$	max.	50	nA
Output capacitance at $f = 1$ MHz	LDC			
$I_E = 0$ ; $-V_{CB} = 5 V$	$C_{C}$	max.	4.5	рF
Input capacitance at $f = 1$ MHz	C			1
$I_C = 0$ ; $-V_{BE} = 0.5 V$	$C_{e}$	max.	10	pF
C of the same	- 0			r
Saturation voltages	Van		0.1	V
$-I_C = 50 \text{ mA}; -I_B = 5 \text{ mA}$	-V <sub>CEsat</sub>	max.	0.4	•
D.C.	-V <sub>BEsat</sub>	max.	0.95	V
D.C. current gain		min.	120	
$-I_C = 2 \text{ mA; } -V_{CE} = 1 \text{ V}$	$h_{FE}$	max.	360	
$-I_C = 50 \text{ mA; } -V_{CE} = 1 \text{ V}$	$h_{FE}$	min.	60	
Noise figure at $R_S = 1 k\Omega$				
$-I_C = 100 \ \mu A; \ -V_{CE} = 5 \ V$				
f = 10  Hz to  15.7  kHz	NF	max.	4	dB
Small signal current gain				
$-V_{CE} = 1V; -I_C = 2 \text{ mA}; f = 1 \text{ KHz}$	$h_{fe}$	min.	120	
VCE 11, 10 2 11111, 1 1 111111	ie	max.	480	
			100	
Transition frequency				
$-V_{CE} = 20V$ ; $-I_{C} = 10 \text{ mA}$ ; $f = 100 \text{ MHz}$	$f_T$	min.	250	MHz
, CE 201, 10 - 10 III 1, 1 - 100 WIIZ	-1	111111.	200	1711 12

## **Customer Notes**

### **Disclaimer**

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